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HITT GAINES 9724808865

NO. 0244 P. 1

**HITT GAINES, P.C.**

Intellectual Property Law &amp; Related Matters

**FACSIMILE TRANSMISSION**

TO: USPTO  
Dinh Thanh Le

FAX NO. (703) 746-9195

FROM: J. Joel Justiss

RE: Serial No.10/033,525  
Attorney Docket No.: PULLELA 4-4  
Request for Corrected Official Filing Receipt

DATE: April 5, 2005

PAGES: 3 (including cover page)

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DOCKET NO. PULLELA 4-4

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Rajasekhar Pullela, et al.

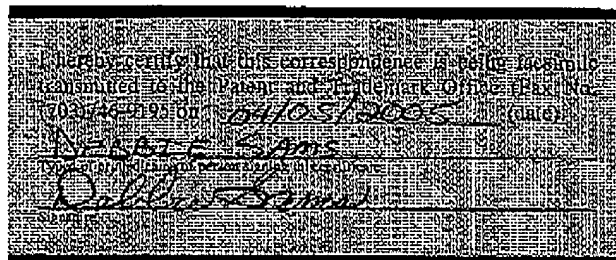
Serial No.: 10/033,525

Filed: December 28, 2001

For: TRANS-ADMITTANCE TRANS-IMPEDANCE  
LOGIC FOR INTEGRATED CIRCUITS

Group No.: 2816

Examiner: Dinh Thanh Le

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:


REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Attached is a copy of the Official Filing Receipt received from the Patent and Trademark Office in this application.

Please amend the Attorney Docket No. on the Official Filing Receipt to: R. PULLELA 4-4

Respectfully submitted,

HITT GAINES, P.C.

  
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## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
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| APPLICATION NUMBER | FILING DATE | GRP ART UNIT | FILE REC'D | ATTY. DOCKET NO.   | DRAWINGS | TOT CLAIMS | IND CLAIMS |
|--------------------|-------------|--------------|------------|--------------------|----------|------------|------------|
| 10/033,525         | 12/28/2001  | 2817         | 870        | 1298/1F986-<br>US2 | 10       | 16         | 3          |

DARBY & DARBY P.C.  
805 Third Avenue  
New York, NY 10022

CONFIRMATION NO. 7823

## UPDATED FILING RECEIPT



\*00000000008529479\*

Date Mailed: 07/29/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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## Assignment For Published Patent Application

LUCENT TECHNOLOGIES, INC.;

## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A CIP OF 09/746,989 12/22/2000  
WHICH IS A CIP OF 09/415,602 10/08/1999 PAT 6,297,706

## Foreign Applications

If Required, Foreign Filing License Granted 01/24/2002

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Early Publication Request: No

## Title

Trans-admittance trans-impedance logic for integrated circuits